

WORKSHOP UPDATED INFO

ROOM F: SINANO-NANOSIL.

“On the convergence between More Moore, More than Moore and Beyond CMOS”.

Formerly entitled: SILICON ON INSULATOR

ROOM D1: NEMSIC.

“Nano-Electro-Mechanical Devices for Integrated Sensing and Switching”.

ROOM C: MOS-AK/GSA.

"Frontiers of the Compact Modeling for Advanced Analog/RF Applications".

ROOM E: ULTRASponder.

“Low Power Electronics for Medical Applications”. FP7 ICT European Project.

COMPUTER ROOM: MULTI-BASE.

“Scalable Multi-tasking Baseband for Mobile Communications”. FP7 ICT European Project.

ROOM D2: ITRS/ERD Workshop.

“Spin and Carbon Based Emerging Logic Devices”.